



Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) EP 1 137 213 A2

(12) EUROPEAN PATENT APPLICATION

(43) Date of publication:
26.09.2001 Bulletin 2001/39

(51) Int Cl.7: H04J 14/02, H04J 14/08

(21) Application number: 01107260.0

(22) Date of filing: 23.03.2001

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR
Designated Extension States:
AL LT LV MK RO SI

• KDD Submarine Cable Systems Inc.
Tokyo (JP)

(72) Inventors:
• Suzuki, Masatoshi
Kamifukuoka-shi, Saitama (JP)
• Edagawa, Noboru
Kamifukuoka-shi, Saitama (JP)

(30) Priority: 24.03.2000 JP 2000083997

(71) Applicants:
• DDI Corporation
Tokyo (JP)

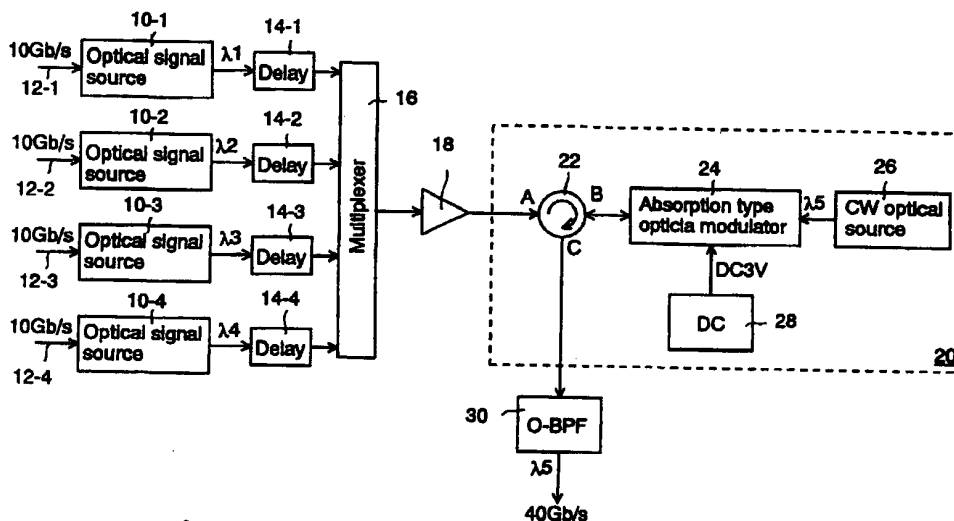
(74) Representative: Schaumburg, Thoenes & Thurn
Postfach 86 07 48
81634 München (DE)

(54) Optical TDM multiplexer, optical TDM demultiplexer, WDM/TDM converter and TDM/WDM converter

(57) An optical TDM multiplexing apparatus to multiplex a plurality of input signals in the optical stage in the time domain according to the invention comprise a plurality of signal light sources to generate optical signals each having a wavelength different from the others to carry each of the plurality of the input signals, a timing adjuster to adjust timings between the respective optical

signals so that each optical signal output from the plurality of the signal light sources is disposed on a time slot different from the others in the time domain, an optical multiplexer to multiplex each optical signal output from the timing adjuster in the wavelength domain and a wavelength converter to convert each wavelength of the output light from the optical multiplexer into a predetermined wavelength.

FIG. 1



EP 1 137 213 A2

Description

FIELD OF THE INVENTION

[0001] This invention relates to an optical TDM multiplexer to multiplex a plurality of signals in the optical stage in the time domain and an optical TDM demultiplexer to demultiplex time-division-multiplexed (TDM) signals in the optical stage.

[0002] This invention also relates to a WDM/TDM converter to convert an optical WDM signal into an optical TDM signal and a TDM/WDM converter to convert an optical TDM signal into an optical WDM signal.

BACKGROUND OF THE INVENTION

[0003] In optical fiber communication, a transmission rate has increased from 10 Gb/s to 20 Gb/s and 40 Gb/s. To cope with such increase of the transmission rate, it requires a device to time-division-multiplex a plurality of signals of 10 Gb/s, and conversely a device to demultiplex a time-division-multiplexed signal into individual signals. It is possible to consider the multiplexing/demultiplexing in both electric and optical stages. However, since the electric performance is limited, the multiplexing/demultiplexing in the optical stage is more desirable.

[0004] For instance, an optical time division multiplexing system is well known that multiplexes optical short pulses with a sufficiently narrow optical pulse width in the optical time domain. To demultiplex a TDM signal in the optical stage, an optical circuit is necessary to logically perform AND operation of a high-speed optical signal and a low-speed short pulse. Representative examples of such AND circuit are a nonlinear loop mirror and an element using four-wave mixing of an optical fiber.

[0005] With regard to the multiplexing, there are problems as follows. That is, to multiplex signals of 10 Gb/s in the electric domain in the time domain, a high-speed electronic circuit is required. In addition, an optical modulator to convert an electric signal into an optical signal generally needs a large driving voltage proportional to an increase of the operation rate while an output voltage of the electronic circuit decreases as the operation rate becomes faster. Accordingly, it is impossible to obtain desired characteristics of such as an extinction ratio using a conventional system that drives an optical modulator with a multiplexed signal obtained through the time-division-multiplexing in the electric domain.

[0006] In addition, in a conventional optical time division multiplexing system to multiplex optical short pulses with sufficiently narrow optical pulse width in the optical time domain, there are strict requirements such as the extinction ratio of each low-speed signal is sufficiently high and the pulse width of each optical pulse is sufficiently narrow in order to avoid noise caused by interference between adjacent pulses. For instance, when four low-speed signals at 10 Gb/s are multiplexed, the pulse width of each 10 Gb/s signal has to be no more

than 1/5 to 1/4 of an objective 40 Gb/s bit time slot (25 ps), namely 5 ps to 6 ps. To realize the above condition, a large and high priced short pulse light source is required and thus the cost is greatly increased.

[0007] Meanwhile, the existing optical AND circuit cannot perform the demultiplexing stably either since it is sensitive to a polarization fluctuation and a temperature fluctuation.

SUMMARY OF THE INVENTION

[0008] It is therefore an object of the present invention to provide an optical TDM multiplexer, an optical TDM demultiplexer, a WDM/TDM converter and a TDM/WDM converter to have superior characteristics to those of prior art.

[0009] Another object of the present invention is to provide an optical TDM multiplexer, an optical TDM demultiplexer, a WDM/TDM converter and a TDM/WDM converter to operate in stable condition and to be realized low-priced.

[0010] Further object of the present invention is to provide an optical TDM multiplexer, an optical TDM demultiplexer, a WDM/TDM converter and a TDM/WDM converter to operate faster than ever.

[0011] An optical TDM multiplexer according to the invention is the apparatus to multiplex a plurality of input signals in the optical stage in the time domain, composed of a plurality of signal light sources to generate optical signals having a wavelength different from each other to carry each of the plurality of the input signals, a timing adjuster to adjust timings between the respective optical signals so that each optical signal output from the plurality of the signal light sources is disposed on a time slot different from the others in the time domain, an optical multiplexer to multiplex optical signals output from the timing adjuster in the wavelength domain, and a wavelength converter to convert each wavelength of the output light from the optical multiplexer into a predetermined wavelength.

[0012] With the above configuration, a plurality of signals at a high bit rate can be multiplexed in the time domain without using a large and expensive short pulse light source. Moreover, the stable operation can be expected.

[0013] It is possible to remove unnecessary components by disposing an optical filter to extract the light having the predetermined wavelength out of the output light from the wavelength converter.

[0014] Preferably, the wavelength converter is composed of a probe light source to generate probe light having the predetermined wavelength, a waveform superimposer applied by the output light from the probe light source and from the optical multiplexer to superimpose a signal waveform of the output light from the optical multiplexer on the probe light, and an outputter to output the probe light transmitted through the waveform superimposer. With this configuration, a wavelength of

signal light as fast as 40 Gb/s is converted into a predetermined wavelength.

[0015] A WDM/TDM converter according to the invention is the apparatus to convert an optical WDM signal composed of a plurality of optical signals each having a wavelength different from the others into an optical TDM signal, composed of a timing adjuster to adjust the timings between the plurality of the optical signals so that the plurality of the optical signals each having a different wavelength from the others to compose the WDM signal are respectively disposed on a time slot different from each other in the time domain, an optical multiplexer to multiplex the respective optical signals output from the timing adjuster in the wavelength domain, and a wavelength converter to convert a wavelength of the output light from the optical multiplexer into a predetermined wavelength.

[0016] According to the above configuration, an optical WDM signal as fast as more than 10 Gb/s can be simply and inexpensively converted into an optical TDM signal. In addition, the operation is stable.

[0017] It is possible to remove unnecessary components by disposing an optical filter to extract the light having the predetermined wavelength out of the output light from the wavelength converter.

[0018] Preferably, the wavelength converter is composed of a probe light source to generate probe light having the predetermined wavelength, a waveform superimposer applied by the output light from the probe light source and from the optical multiplexer to superimpose a signal waveform of the output light from the optical multiplexer on the probe light, and an outputter to output the probe light transmitted through the waveform superimposer. According to this configuration, a wavelength of signal light as fast as 40 Gb/s can be converted into a predetermined wavelength. That is, an optical TDM signal at a bit rate of more than 40 Gb/s is easily produced.

[0019] An optical TDM demultiplexer according to the invention is the apparatus to demultiplex an optical TDM signal having n (n is an integer not less than 2) time slots into individual signals on the respective time slots, composed of a pulse light generator to generate optical pulse trains each having a wavelength different from the others at timings each belonging to a different time slot from the others, an optical multiplexer to multiplex the n optical pulses output from the pulse light generator in the wavelength domain, a waveform superimposer applied by the optical TDM signal and the output light from the optical multiplexer to superimpose a signal waveform of the optical TDM signal onto the output light from the multiplexer, and a wavelength demultiplexer to demultiplex the output light from the optical multiplexer transmitted through the waveform superimposer into respective wavelengths.

[0020] With this configuration, it is possible to demultiplex an optical TDM signal composed of optical signals as fast as 10 Gb/s or more into individual signals. In ad-

dition, it is realized with a simple and inexpensive configuration, and the operation is stable.

[0021] Preferably, the optical TDM demultiplexer is further composed of a photodetector to convert the optical signal having a predetermined wavelength output from the wavelength demultiplexer into an electric signal and a clock generator to generate a clock signal synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with the clock output from the clock generator. With this configuration, an optical TDM signal is demultiplexed into individual signals without fail.

[0022] Preferably, the optical TDM demultiplexer is further composed of a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust time slot dispositions of the respective optical pulse trains generated by the pulse light generator according to the detected result. Alternatively, the optical TDM demultiplexer is further composed of an optical delay device capable of changing delay time to delay the optical TDM signal before entering the waveform superimposer and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust the delay time of the optical delay device according to the detected result so that the optical pulse train output from the optical multiplexer and the optical TDM signal delayed by the optical delay device have predetermined phase relations. With these configurations, it is possible to determine the correspondence between a signal after TDM demultiplexing and a time slot to which the signal belongs before the demultiplexing. The latter configuration is simpler than the former.

[0023] Preferably, the optical TDM demultiplexer is further composed of a photodetector to convert the optical TDM signal into an electric signal and a clock generator to generate a clock signal at $1/n$ frequency synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with the clock output from the clock generator. According to this configuration, an optical TDM signal also can be demultiplexed into individual signals without fail.

[0024] A TDM/WDM converter according to the invention is the apparatus to convert an optical TDM signal having n (n is an integer no less than 2) time slots into an optical WDM signal having n optical signals each having a different wavelength from the others, composed of a pulse light generator to generate n optical pulse trains each having a wavelength different from the others at timings each belonging to a time slot different from the others, an optical multiplexer to multiplex the n optical pulse trains output from the pulse light generator and a waveform superimposer applied by the optical TDM signal and output light from the optical multiplexer to superimpose a signal waveform of the optical TDM

signal onto the output light from the multiplexer. With this configuration, optical signals on different time slots included in a high-speed optical TDM signal can be converted into optical signals each having a wavelength different from the others.

[0025] Preferably, the TDM/WDM converter is further composed of an extractor to extract signal light having a predetermined wavelength out of the output light from the optical multiplexer transmitted through the waveform superimposer, a photodetector to convert the output light from the extractor into an electric signal and a clock generator to generate a clock signal synchronized with a clock component included in the output from the photodetector. In addition, the pulse light generator generates respective optical pulse trains in synchronization with the clock output from the clock generator. According to this configuration, an optical TDM signal can be converted into an optical WDM signal without fail.

[0026] Preferably, the TDM/WDM converter is further composed of a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust time slot dispositions of the respective optical pulse trains generated by the pulse light generator according to the detected result. Alternatively, the optical TDM/WDM converter is further composed of an optical delay device capable of changing delay time to delay the optical TDM signal before entering the waveform superimposer and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust the delay time of the optical delay device according to the detected result so that the optical pulse train output from the optical multiplexer and the optical TDM signal delayed by the optical delay device have predetermined phase relations. With these configurations, it is possible to determine the correspondence between a signal after TDM demultiplexing and a time slot to which the signal belongs before the demultiplexing. The latter configuration is simpler than the former.

[0027] Preferably, the TDM/WDM converter is further composed of a photodetector to convert the optical TDM signal into an electric signal and a clock generator to generate a clock signal at a $1/n$ frequency synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with the clock output from the clock generator. According to this configuration, an optical TDM signal also can be demultiplexed into individual signals without fail.

BRIEF DESCRIPTION OF THE DRAWINGS

[0028] The above and other objects, features and advantages of the present invention will be apparent from the following detailed description of the preferred embodiments of the invention in conjunction with the accompanying drawings, in which:

FIG. 1 shows a schematic block diagram of an embodiment of a TDM multiplexer according to the invention;

FIGS. 2 (1) through (6) show waveform examples of the embodiment shown in FIG. 1;

FIG. 3 shows a schematic block diagram of an embodiment as a WDM/TDM converter;

FIG. 4 shows a schematic block diagram of an embodiment of a TDM demultiplexer according to the invention;

FIGS. 5(1) through (11) show waveform examples of the embodiment shown in FIG. 4;

FIG. 6 shows a schematic block diagram of a modification of the embodiment shown in FIG. 4; and

FIG. 7 shows a schematic block diagram of another modification of the embodiment shown in FIG. 4.

DESCRIPTION OF THE PREFERRED EMBODIMENT

[0029] Embodiments of the invention are explained below in detail with reference to the drawings.

[0030] FIG. 1 shows a schematic block diagram of an embodiment of an optical TDM multiplexer according to the invention. This embodiment is to time-division-multiplex 4 signals of 10 Gb/s and to generate a signal of 40 Gb/s. FIGS. 2(1) through (6) show timing charts of the embodiment shown in FIG. 1.

[0031] Optical signal sources 10-1 through 10-4 generate RZ optical signals of 10 Gb/s having different wavelengths λ_1 through λ_4 according to data signals 12-1 through 12-4 of 10 Gb/s, respectively. Although the optical pulse width is better as narrow as possible, it is applicable as far as it fits in a time slot of 40 Gb/s after time-division-multiplexing.

[0032] Optical delay devices 14-1 through 14-4 delay the optical signals output from the respective optical signal sources 10-1 through 10-4 so as to enter predetermined different time slots of 40 Gb/s respectively. FIGS. 2(1) through (4) show waveform examples of the optical signals output from the optical delay devices 14-1 through 14-4. In other words, each delay time of the optical delay devices 14-1 through 14-4 is set so that the output light from the optical delay device 14-1 enters a first time slot, the output light from the optical delay device 14-2 enters a second time slot, the output light from the optical delay device 14-3 enters a third time slot, and the output light from the optical delay device 14-4 enters a fourth time slot in the four time slots obtained when 40 Gb/s is divided into four 10 Gb/s in the time domain. It is obvious that an electric delay device having the similar function can be disposed on each input side of the optical signal sources 10-1 through 10-4.

[0033] An optical multiplexer 16 multiplexes the respective optical signal output from the optical delay devices 14-1 through 14-4 in the wavelength domain. FIG. 2(5) shows an example of optical intensity waveform of the output light from the optical multiplexer 16. When two optical pulses exist on adjacent time slots, they are

combined together and form a single pulse with a slightly depressed peak. Such combination of optical pulses does not interfere with this embodiment.

[0034] An optical amplifier 18 amplifies the output light from the optical multiplexer 16 and applies it to a wavelength converter 20. The wavelength converter 20 is a device to convert a wavelength of the input light into a wavelength λ_5 , namely to generate light of the wavelength λ_5 having an essentially identical waveform (or a reverse waveform) to that of the input light. Although the wavelength of the input light and the wavelength λ_5 after the conversion can be the same, desirably they should be different to avoid the interference. Even if the input light contains light having a plurality of wavelengths, the wavelength converter 20 outputs the light of the wavelength λ_5 having a waveform according to that of the combined light intensity. The details of the wavelength converter 20 are disclosed in Japanese Laid-Open Patent Publication No. 10-78595 and U.S. Patent 5,959,764 to correspond to the Japanese Laid-Open patent Publication, for instance. Accordingly, on the details of the wavelength converter 20, those references will be cited.

[0035] The output light from the optical amplifier 18 enters a port A of an optical circulator 22 and outputs from a port B to enter a waveguide transmission type InGaAsP electroabsorption optical modulator 24. A CW light source 26 generates CW laser light (probe light) of the wavelength λ_5 . The CW laser light output from the CW optical source 26 enters the optical modulator 24 so as to propagate in the opposite direction to the input light from the port B of the optical circulator 22. A DC power supply 28 applies DC voltage, e.g. DC 3V, to the optical modulator 24. Although it is not shown in the drawing, an electric cooling unit is disposed to stabilize the temperature of the optical modulator 24. As described in detail in the above publication, the light (the output light from the multiplexer 16) from the port B of the optical circulator 22 and the output light from the CW light source 26 interact with each other in the optical modulator 24, and consequently the optical intensity waveform of the light having the wavelength λ_5 which is output toward the port C of the optical circulator 22 from the optical modulator 24 represents a copy of the optical intensity waveform of the output light from the multiplexer 16 as shown in FIG. 2(6).

[0036] The optical circulator 22 transmits the light that entered the port B from the optical modulator 24 to a port C and applies it to an optical bandpass filter 30 from the port C. The bandpass filter 30 is set to exclusively transmit the wavelength λ_5 alone. Unnecessary wavelength components such as components of the wavelengths λ_1 through λ_4 are removed by the optical bandpass filter 30. The output of the optical bandpass filter 30 is signal light of 40 Gb/s in which the four data 12-1 through 12-4 of 10 Gb/s are time-division-multiplexed.

[0037] The embodiment shown in FIG. 1 is also applicable to a device to convert an optical WDM signal into

an optical TDM signal. For a wide use, timings between optical signals of respective wavelengths entered the optical delay device 14-1 through 14-4 is measured, and the delay time of each of the optical delay devices 14-1 through 14-4 is automatically controlled so that the optical signals are located on different time slots after the TDM respectively. Needless to say, in such case that the timings between the input optical pulses of the optical delay devices 14-1 through 14-4 are already known, the delay time of each of the optical delay devices 14-1 through 14-4 can be preset.

[0038] FIG. 3 shows a schematic block diagram of an embodiment to convert an optical WDM signal from an optical transmission line into an optical TDM signal. Elements identical to those in FIG. 1 are labeled with common reference numerals.

[0039] An optical WDM signal 31 composed of optical signals of 10 Gb/s having wavelengths λ_1 through λ_4 enters a demultiplexer 32 from an optical fiber transmission line. The demultiplexer 32 demultiplexes the optical WDM signal 31 into individual optical signals having the wavelengths λ_1 through λ_4 and applies them to optical receivers 34-1 through 34-4 respectively. The optical receivers 34-1 through 34-4 convert the input optical signals into electric signals and apply them to optical transmitters 36-1 through 36-4 respectively. The optical transmitters 36-1 through 36-4 convert the optical signals from the optical receivers 34-1 through 34-4 into optical signals of RZ optical pulse having wavelengths λ_1 through λ_4 and apply them to optical delay devices 38-1 through 38-4 respectively.

[0040] The optical receivers 34-1 through 34-4 also extract a clock component from the input signal light. The clocks extracted by the optical receivers 34-1 through 34-3 are applied to phase comparators 39-1 through 39-3 while the clocks extracted by the optical receiver 34-4 are applied to the optical delay device 38-4 and the phase comparators 39-1 through 39-3 as a reference clock. Each of the phase comparators 39-1 through 39-3 compares phase of the clock (the reference clock) from the optical receiver 34-4 to the clocks from the associated one of the optical receivers 34-1 through 34-3 and controls the delay time of the associated one of the optical delay devices 38-1 through 38-3 so that optical signals to be output from the optical transmitters 36-1 through 36-3 are respectively located on a first time slot, a second time slot and a third time slot after the time-division-multiplexing. The delay time of the optical delay device 38-4 is controlled according to the reference clock from the optical receiver 34-4 so that the signal light output from the optical transmitter 36-4 is located on a fourth time slot after the time-division-multiplexing. As stated above, by controlling the delay time of the optical delay devices 38-1 through 38-4, the optical signals output from the optical delay devices 38-1 through 38-4 are located at timings shown in FIGS. 2(1) through (4). The operation after this is the same with the explanation of the embodiment in FIG. 1 and

thus TDM signal light at 40 Gb/s is finally obtained.

[0041] The optical receiver 34-1 through 34-4 and the optical transmitter 36-1 through 36-4 function as a device to shape a waveform of the signal light as a whole. Needless to say, the optical transmitter 36-1 through 36-4 can output an optical signal having a wavelength different from that of an optical signal to enter the optical receiver 34-1 through 34-4. In this point of view, the optical receiver 34-1 through 34-4 and the optical transmitter 36-1 through 36-4 function as a wavelength converter as a whole. Naturally, when such functions of the waveform shaping and waveform conversion are unnecessary, photodetectors and clock extracting circuits to extract a clock of the signal light having each wavelength should be disposed instead of the optical receiver 34-1 through 34-4 and the optical transmitter 36-1 through 36-4.

[0042] Fig. 4 shows a schematic block diagram of an embodiment of a TDM demultiplexer according to the invention. The embodiment shown in FIG. 4 demultiplexes a signal of 40 Gb/s into respective four signals of 10 Gb/s. FIGS. 5(1) through (11) each shows an optical waveform of each part in the embodiment shown in FIG. 4.

[0043] An 40 Gb/s optical signal 40 of a single wavelength (here, λ_5) entered a port A of an optical circulator 42 is transmitted to a port B and applied to a waveguide transmission type InGaAsP electroabsorption optical modulator 44 from the port B. FIG. 5(1) shows a waveform example of the input signal light 40.

[0044] Pulse light sources 46-1 through 46-4 to generate pulse light of 10 Gb/s after the TDM demultiplexing are provided as many as the number of the signals after the TDM demultiplexing and laser-oscillate at wavelengths λ_1 through λ_4 , which are different from each other, respectively. The optical pulses output from the pulse light sources 46-1 through 46-4 are temporally adjusted in the time domain by the optical delay devices 48-1 through 48-4 respectively and applied to a multiplexer 50. Each delay time of the optical delay devices 48-1 through 48-4 is adjusted so that each optical pulse output from the pulse light sources 46-1 through 46-4 is disposed on a different time slot from the other when 40 Gb/s is divided into four time slots of 10 Gb/s. FIGS. 5(1) through (4) show waveform examples of the output light from the optical delay devices 48-1 through 48-4 respectively. The multiplexer 50 multiplexes the output light from the optical delay devices 48-1 through 48-4 in the wavelength domain. FIG. 5(6) shows a waveform example of the output light from the multiplexer 50. The output light from the multiplexer 50 is composed of pulse light of 40 Gb/s although the optical signals having the four wavelengths are periodically mixed.

[0045] It is necessary to synchronize the optical pulses output from the pulse light sources 46-1 through 46-4 with the input signal light 40. In addition, it is necessary to set the relative delay amount of the optical delay devices 48-1 through 48-4 according to the input signal

light 40 in order to correspond the wavelengths λ_1 through λ_4 of the output light from the pulse light sources 46-1 through 46-4 and the four time slots of the input signal light 40. Means for the above operations are to be described later.

[0046] The output light from the multiplexer 50 is amplified by an optical amplifier 52 and applied to the optical modulator 44 as probe light in the opposite transmission direction to the 40 Gb/s signal light. A DC power source 54 applies a constant DC voltage to the optical modulator 44. The optical modulator 44 functions as a wavelength converter to input pulse light as probe light. The optical modulator 44, more specifically, functions as an optical AND circuit to perform an AND-operation between the input 40 Gb/s signal light 40 and the pulse train output from the optical amplifier 52 or as an optical gate circuit to selectively transmit an optical pulse included in the optical pulse train from the optical amplifier 52 according to the input signal light 40 of 40 Gb/s as a window. FIG. 5(7) shows a waveform of the light which output from the optical amplifier 53 and transmitted through the optical modulator 44. Such operation of the optical modulator 44 is described in detail with reference to FIGS. 6 through 8 in Japanese Laid-Open Patent Publication No. 10(1998)-78595 and U.S. Patent No. 5,959,764.

[0047] The optical pulses of wavelengths λ_1 through λ_4 output from the optical modulator 44 are applied to the port B of the optical circulator 42, transmitted to a port C and sent to a wavelength demultiplexer 56 from the port C. The wavelength demultiplexer 56 demultiplexes the light from the port C of the optical circulator 42 into different wavelengths and outputs them. Accordingly, the wavelength demultiplexer 56 outputs 10 Gb/s optical signals 58-1 through 58-4 having the wavelengths λ_1 through λ_4 being different from each other. FIGS. 5(8) through (11) show waveform examples of the signal light 58-1 through 58-4 respectively.

[0048] A photodetector 60 converts the optical signal 58-4 having the wavelength λ_4 into an electric signal. A bandpass filter 62 extracts a clock component of 10 Gb/s out of the output from the photodetector 60 and applies it to a PLL circuit 64. The PLL circuit 64 of a well-known configuration generates a 10 GHz clock signal synchronized with the output from the BPF 62. The PLL circuit 64 is composed of, for example, a 10 GHz voltage control oscillator (VCO) 66, a comparator 68 to compare phase between the output from the BPF 62 and that from the VCO 66 and to output an error signal indicating a phase error and a voltage generator 70 to generate a voltage signal to control an oscillation frequency of the VCO 66 according to the error signal output from the comparator 68.

[0049] The 10 GHz clock generated by the PLL circuit 64 is applied to the pulse light sources 46-1 through 46-4. The pulse light sources 46-1 through 46-4 generate pulse light having a frequency equal to that of the clock from the PLL circuit 64. The pulse light sources

46-1 through 46-4 can be composed of either a mode locked laser to generate pulse light according to the clock from the PLL circuit 64 or a combination of a CW laser and an electroabsorption optical modulator to generate pulse light by modulating intensity of output light from the CW laser according to the clock from the PLL circuit 64. According to the above configuration, the pulse light sources 46-1 through 46-4 generate pulse light synchronized with the input signal light 40. However, the bit rate of the optical pulse output from each of the pulse light sources 46-1 through 46-4 is one fourth of the input signal light.

[0050] A time slot ID detector 72 detects information to identify a time slot, namely time slot ID, out of the output signal from the photodetector 60. The time slot ID, for example, is the information to show on which time slot after the TDM each signal to be multiplexed in the time domain is disposed and can be the destination of the signal. With this operation, it is determined to which time slot on the input signal light 40 the signal 58-4 should be disposed currently. The time slot ID detector 72 adjusts the delay time of the optical delay devices 48-1 through 48-4 according to the detected result so that the optical signals 58-1 through 58-4 are disposed to the first time slot, the second time slot, the third time slot and the forth time slot on the input 40 Gb/s signal light 40 respectively. When the time delay in the pulse light sources 46-1 through 46-4 is disregarded, the delay time of the optical delay devices 46-1 through 46-4 should be any one of 0, 25 ps, 50 ps and 75 ps, and the time slot ID detector 72 assigns those time to the respective optical delay devices 48-1 through 48-4 as delay time. However, it is obvious that each delay time of the optical delay devices 48-1 through 48-4 is different from the others.

[0051] When the optical signals 58-1 through 58-4 are transmitted on the same optical fiber, the demultiplexing of the wavelength by the demultiplexer 56 is unnecessary, and the output light from the port C of the optical circulator 42 is applied to the optical fiber instead. However, even in such case, an optical filter is necessary to extract the signal light having the wavelength λ_4 and to apply it to the photodetector 60. As easily understandable, the embodiment shown in FIG. 4 also functions as a converter to convert an optical TDM signal into an optical WDM signal.

[0052] In the embodiment shown in FIG. 4, the optical delay devices 48-1 through 48-4 control the cross timing between the output optical pulses from the pulse light sources 46-1 through 46-4 in the time domain. However, it is also applicable that the optical pulse phase of the input signal light 40 is controlled according to the detected result of the time slot ID detector 72 while the cross delay time of the optical delay devices 48-1 through 48-4 is fixed. FIG. 6 shows a schematic block diagram of such modified embodiment. Elements common to those in FIG. 4 are numbered with the same reference numerals. However, the delay time of the optical delay devices

48-1 through 48-4 are generally fixed to 0, 25 ps, 50 ps and 50 ps respectively when uneven response delay of the pulse light sources 46-1 through 46-4 is disregarded.

[0053] In an embodiment shown in FIG. 6, the input signal light 40 enters the port A of the optical circulator 42 via an optical delay device 74 having valuable delay time. The delay time of the optical delay devices 48-1 through 48-4 are adjusted and fixed in advance. When it is possible to disregard the cross time deviation of the output optical pulses from the pulse light sources 46-1 through 46-4, the delay time of any one of the optical delay devices, for example the optical delay device 48-1, can be zero. That is, the optical delay device 48-1 can be omitted.

[0054] The time slot ID detector 72 adjusts the delay time of the optical delay device 74 according to the detected result so that the signal light 58-4 carries a signal on the fourth time slot. Consequently, the timings between the input signal light 40 and the output pulse light from the optical amplifier 52 are controlled so that the signal light 58-1 carries a signal on the first time slot on the input signal light 40, the signal light 58-2 carries a signal on the second time slot, the signal light 58-3 carries a signal on the third time slot, and the signal light 58-4 carries the signal light on the fourth slot.

[0055] In FIG. 4, although the pulse light sources 46-1 through 46-4 are synchronized with the input signal light 40 using the 10 GHz clock synchronized with the signal obtained by demultiplexing a TDM signal, it is also applicable that the 10 GHz clock component is generated from the input signal light 40 and synchronized with the pulse light sources 46-1 through 46-4. FIG. 7 shows a schematic block diagram of such modified embodiment. Elements common to those in FIG. 4 are numbered with the same reference numerals.

[0056] The demultiplexer 76 divides the input signal light 40 of a 40 Gb/s TDM signal into two portions and applies one portion to the port A of the optical circulator 42 and the other to a photodetector 78. The photodetector 78 converts the input light into an electric signal. A bandpass filter 80 extracts a 40 GHz component from the output from the photodetector 78 and applies it to a 1/4 frequency divider 82. The frequency divider 82 generates a 10 GHz signal synchronized with the output from the BPF 80. That is, the frequency divider 82 is composed of a 10 GHz voltage control oscillation (VCO) 84, a quadrupler 86 to quadruple output frequency of the VCO 84, a comparator 88 to compare phase between the output from the BPF 80 and that of the quadrupler 86 and to outputs a error signal indicating a phase error, and a voltage generator 90 to generate a voltage signal to control the oscillation frequency of the VCO 84 according to the error signal output from the comparator 88. With this configuration, the output from the VCO 84 is synchronized with the output from the BPF 80, namely the input signal light 40.

[0057] The 10 GHz clock output from the VCO 84 of the frequency divider 82 is applied to the pulse light

sources 46-1 through 46-4. The pulse light sources 46-1 through 46-4 generate pulse light having the same frequency with the clock from the frequency divider 82. The operation after this is identical to the description of the embodiment shown in FIG. 4.

[0058] Obviously, such modification shown in FIG. 6 is also applicable to the modified embodiment shown in FIG. 7. That is, an optical delay device with variable delay time is disposed immediately before the demultiplexer 76, and the delay time is controlled according to a detected result of the time slot ID detector 72.

[0059] As described above, a signal of 40 Gb/s is demultiplexed into four signals of 10 Gb/s. Since the optical pulse width of the pulse light sources 46-1 through 46-4 is not necessarily as narrow as that of the conventional art, it can be realized with low costs. By increasing the number of the pulse light sources 46-1 through 46-4, an optical TDM signal at even higher speed can be demultiplexed into a plurality of signals in the time domain.

[0060] While the invention has been described with reference to a specific embodiment, it will be apparent to those skilled in the art that various changes and modifications can be made to the specific embodiment without departing from the spirit and scope of the invention as defined in the claims.

[0061] As is readily understandable from the aforementioned description, according to the invention, a plurality of signals can be multiplexed in the time domain with a simple configuration. In addition, a signal at a high speed can be demultiplexed into a plurality of signals at a low speed in the time domain.

Claims

1. An optical TDM multiplexing apparatus to multiplex a plurality of input signals in the optical stage in the time domain, comprising:

a plurality of signal light sources to generate optical signals each having a wavelength different from the others to carry each of the plurality of the input signals;

a timing adjuster to adjust timings between the respective optical signals so that each optical signal output from the plurality of the signal light sources is disposed on a time slot different from the others in the time domain;

an optical multiplexer to multiplex each optical signal output from the timing adjuster in the wavelength domain; and

a wavelength converter to convert each wavelength of the output light from the optical multiplexer into a predetermined wavelength.

2. The optical TDM multiplexing apparatus of claim 1 further comprising an optical filter to extract light having the predetermined wavelength out of the

output light from the wavelength converter.

3. The optical TDM multiplexing apparatus of claim 1 or 2 wherein the wavelength converter comprises a probe light source to generate probe light having the predetermined wavelength, a waveform superimposer applied by the output light from the probe light source and from the optical multiplexer to superimpose a signal waveform of the output light from the optical multiplexer on to the probe light, and an outputter to output the probe light transmitted through the waveform superimposer.

4. A WDM/TDM converter to convert an optical WDM signal composed of a plurality of optical signals each having a wavelength different from the others into an optical TDM signal, comprising:

a timing adjuster to adjust timings between the plurality of the optical signals so that the plurality of the optical signals each having a wavelength different from the others and consisting of the Optical WDM signal are respectively disposed on a time slot different from each other in the time domain;

an optical multiplexer to multiplex each signal light output from the timing adjuster in the wavelength domain; and

a wavelength converter to convert a wavelength of the output light from the optical multiplexer into a predetermined wavelength.

5. The WDM/TDM converter of claim 4 further comprising an optical filter to extract light having the predetermined wavelength out of the output light from the wavelength converter.

6. The WDM/TDM converter of claim 4 or 5 wherein the wavelength converter comprises a probe light source to generate probe light having the predetermined wavelength, a waveform superimposer applied by the output light from the probe light source and from the optical multiplexer to superimpose a signal waveform of the output light from the optical multiplexer onto the probe light, and an outputter to output the probe light transmitted through the waveform superimposer.

7. An optical TDM demultiplexer to demultiplex an optical TDM signal having n (n is an integer no less than 2) time slots into individual signals on the respective time slots, comprising:

a pulse light generator to generate n optical pulse trains, each having a wavelength different from the others at timings each belonging to a time slot different from the others;

an optical multiplexer to multiplex the n optical

- pulse trains output from the pulse light generator in the wavelength domain;
 a waveform superimposer applied by the optical TDM signal and the output light from the optical multiplexer to superimpose a signal waveform of the optical TDM signal onto the output light from the multiplexer; and
 a wavelength demultiplexer to demultiplex the output light from the optical multiplexer transmitted through the waveform superimposer into respective wavelengths.
8. The optical TDM demultiplexer of claim 7 further comprising a photodetector to convert the optical signal having a predetermined wavelength output from the wavelength demultiplexer into an electric signal and a clock generator to generate a clock signal synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with the clock output from the clock generator.
 9. The optical TDM demultiplexer of claim 8 further comprising a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust time slot dispositions of the respective optical pulse trains generated by the pulse light generator according to the detected result.
 10. The optical TDM demultiplexer of claim 8 or 9 further comprising an optical delay device having variable delay time to delay the optical TDM signal before entering the waveform superimposer and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust the delay time of the optical delay device according to the detected result so that the optical pulse train output from the optical multiplexer and the optical TDM signal delayed by the optical delay device have predetermined phase relations.
 11. The optical TDM demultiplexer of anyone of claim 7 to 10 further comprising a photodetector to convert the optical TDM signal into an electric signal and a clock generator to generate a clock signal at a $1/n$ frequency synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with the clock output from the clock generator.
 12. The optical TDM demultiplexer of claim 11 further comprising a photodetector to convert the optical signal having the predetermined wavelength output from the wavelength demultiplexer into an electric signal and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust time slot dispositions of the respective optical pulse trains generated by the pulse light generator according to the detected result.
 13. The optical TDM demultiplexer of claim 11 or 12 further comprising an optical delay device having variable delay time to delay the optical TDM signal before entering the waveform superimposer and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust the delay time of the delay device according to the detected result so that the optical pulse train output from the optical multiplexer and the optical TDM signal delayed by the optical delay device have predetermined phase relations.
 14. The optical TDM demultiplexer of anyone of claim 7 to 13 wherein the pulse light generator comprises n pulse light sources to generate optical pulses each having a wavelength different from the others and a timing adjuster to adjust timings between the optical pulses output from the n pulse light sources so that each optical pulse output from the n pulse light sources belongs to a time slot different from the others.
 15. A TDM/WDM converter to convert an optical TDM signal having n (n is a integer no less than 2) time slots into an optical WDM signal having n optical signals each having a wavelength different from the others, comprising:
 - a pulse light generator to generate n optical pulse trains, each having a wavelength different from the others at timings each belonging to a time slot different from the others;
 - an optical multiplexer to multiplex the n optical pulse trains output from the pulse light generator; and
 - a waveform superimposer applied by the optical TDM signal and the output light from the optical multiplexer to superimpose a signal waveform of the optical TDM signal on the output light from the multiplexer.
 16. The TDM/WDM converter of claim 15 further comprising an extractor to extract signal light having a predetermined wavelength out of the output light from the optical multiplexer transmitted through the waveform superimposer, a photodetector to convert the optical signal output from the extractor into an electric signal and a clock generator to generate a clock signal synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with

the clock output from the clock generator.

17. The TDM/WDM converter of claim 16 further comprising a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust time slot dispositions of the respective optical pulse trains generated by the pulse light generator according to the detected result. 5
18. The TDM/WDM converter of claim 16 or 17 further comprising an optical delay device having variable delay time to delay the optical TDM signal before entering the waveform superimposer and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust the delay time of the optical delay device according to the detected result so that the optical pulse train output from the optical multiplexer and the optical TDM signal delayed by the optical delay device have predetermined phase relations. 10 15 20
19. The TDM/WDM converter of anyone of claim 15 to 18 further comprising a photodetector to convert the optical TDM signal into an electric signal and a clock generator to generate a clock signal at a $1/n$ frequency synchronized with a clock component included in the output from the photodetector, wherein the pulse light generator generates the respective optical pulse trains in synchronization with the clock output from the clock generator. 25 30
20. The TDM/WDM converter of claim 19 further comprising an extractor to extract signal light having the predetermined wavelength out of the output from the optical multiplexer transmitted through the waveform superimposer, a photodetector to convert the output from the extractor into an electric signal and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust time slot dispositions of the respective optical pulse trains generated by the pulse light generator according to the detected result. 35 40
21. The TDM/WDM converter of claim 19 or 20 further comprising an optical delay device having variable delay time to delay the optical TDM signal before entering the waveform superimposer and a time slot detector to detect a time slot discrimination signal out of the output from the photodetector and to adjust the delay time of the optical delay device according to the detected result so that the optical pulse train output from the optical multiplexer and the optical TDM signal delayed by the optical delay device have predetermined phase relations. 45 50 55
22. The TDM/WDM converter of anyone of claim 15 to 21 wherein the pulse light generator comprises n

pulse light sources to generate optical pulse trains each having a different wavelength from the others and a timing adjuster to adjust timings between the respective optical pulse trains output from the n pulse light sources so that each of the optical pulse trains output from the n pulse light sources belongs to a time slot different from the others.

FIG. 1

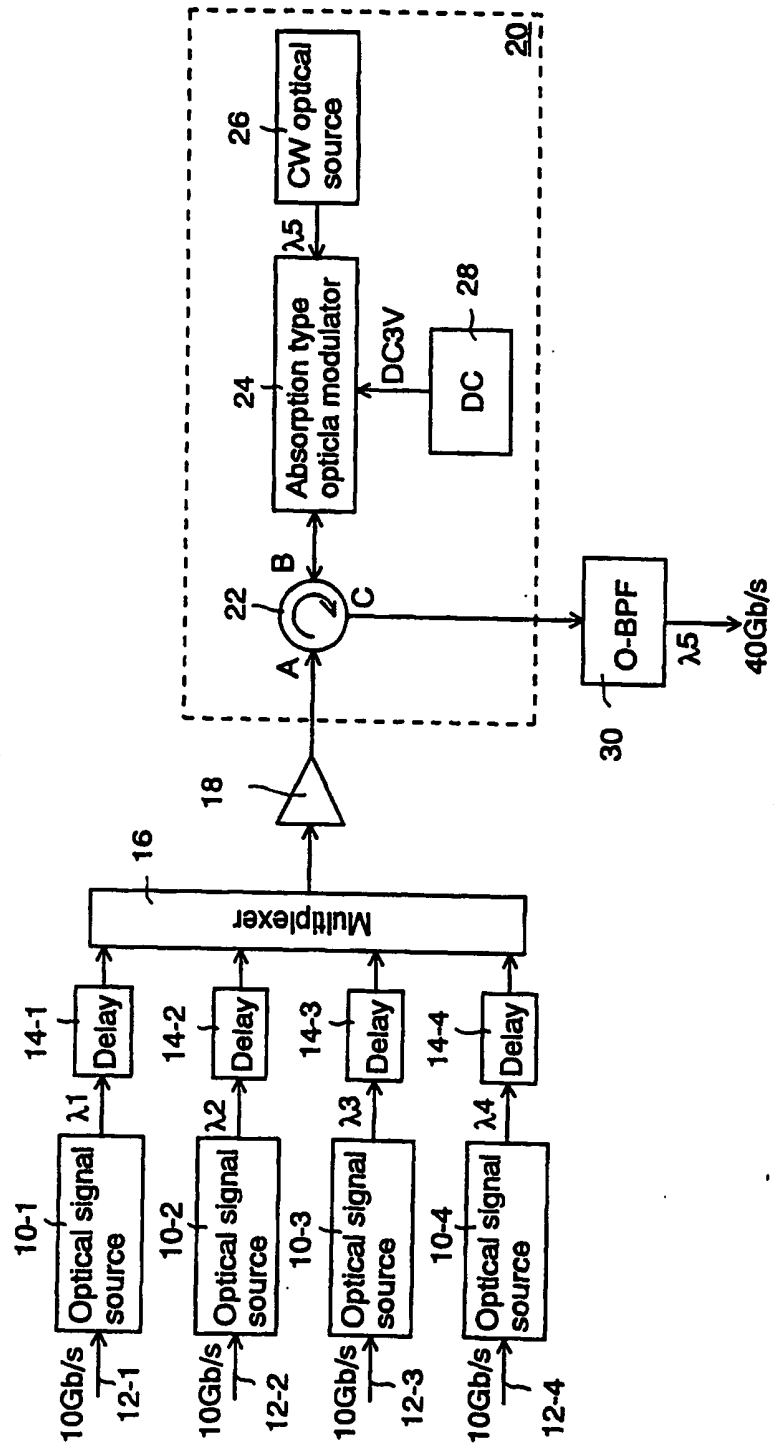


FIG. 2

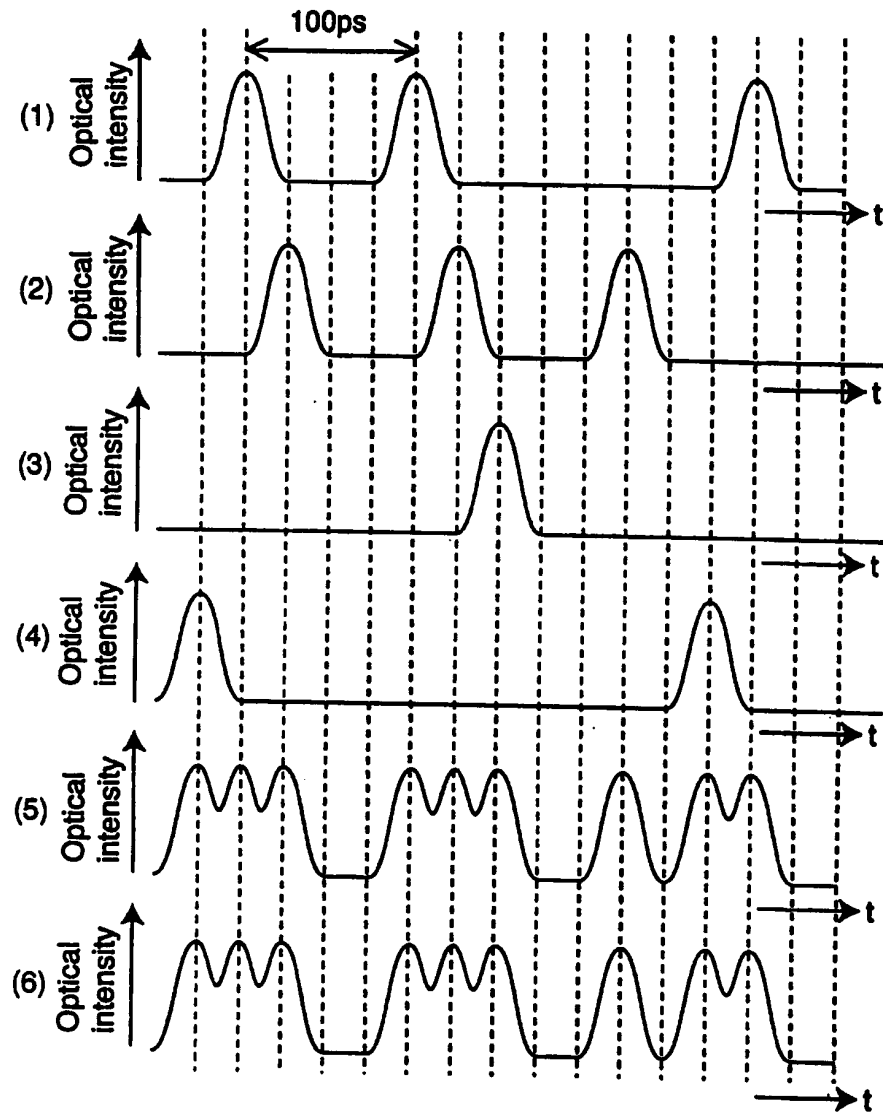


FIG. 3

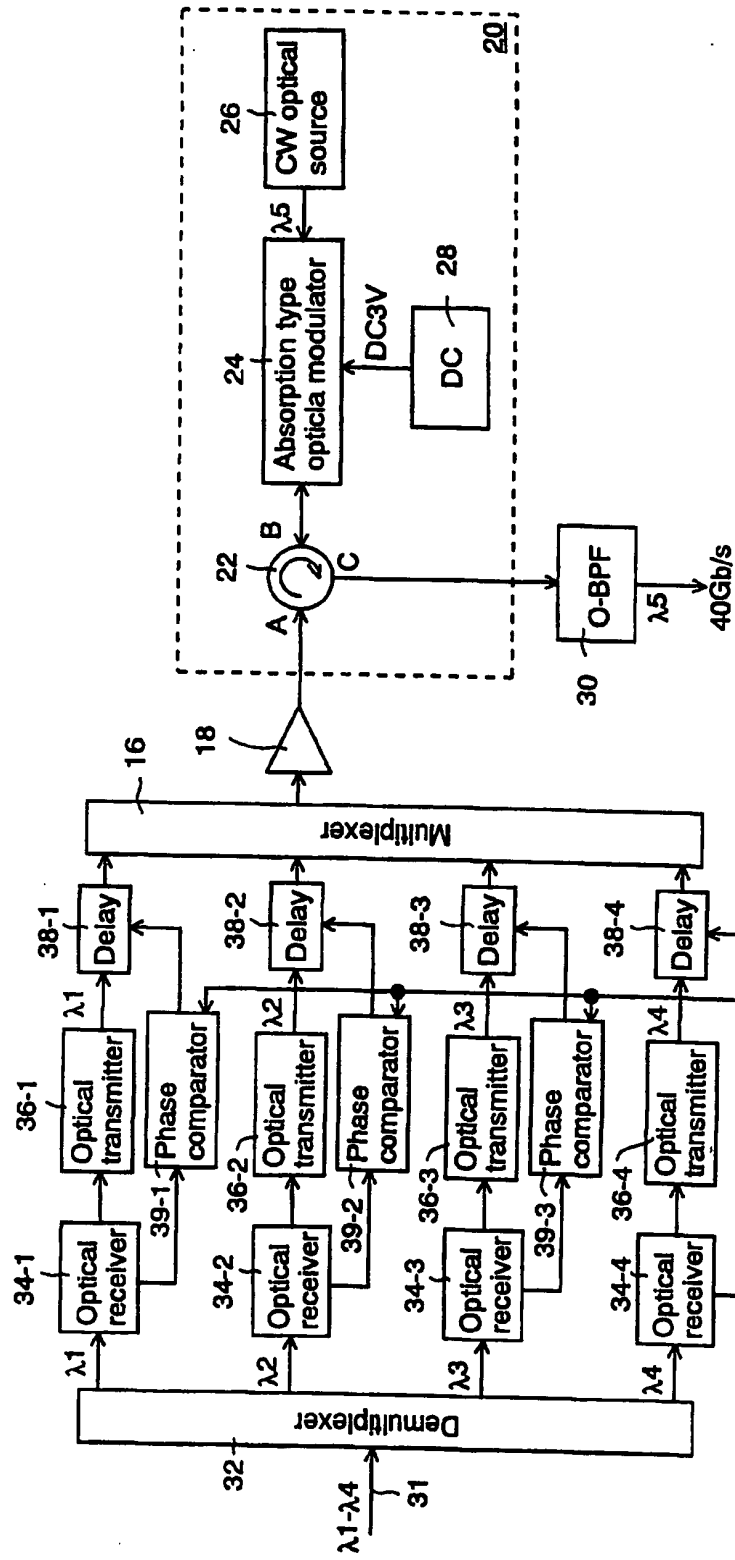


FIG. 4

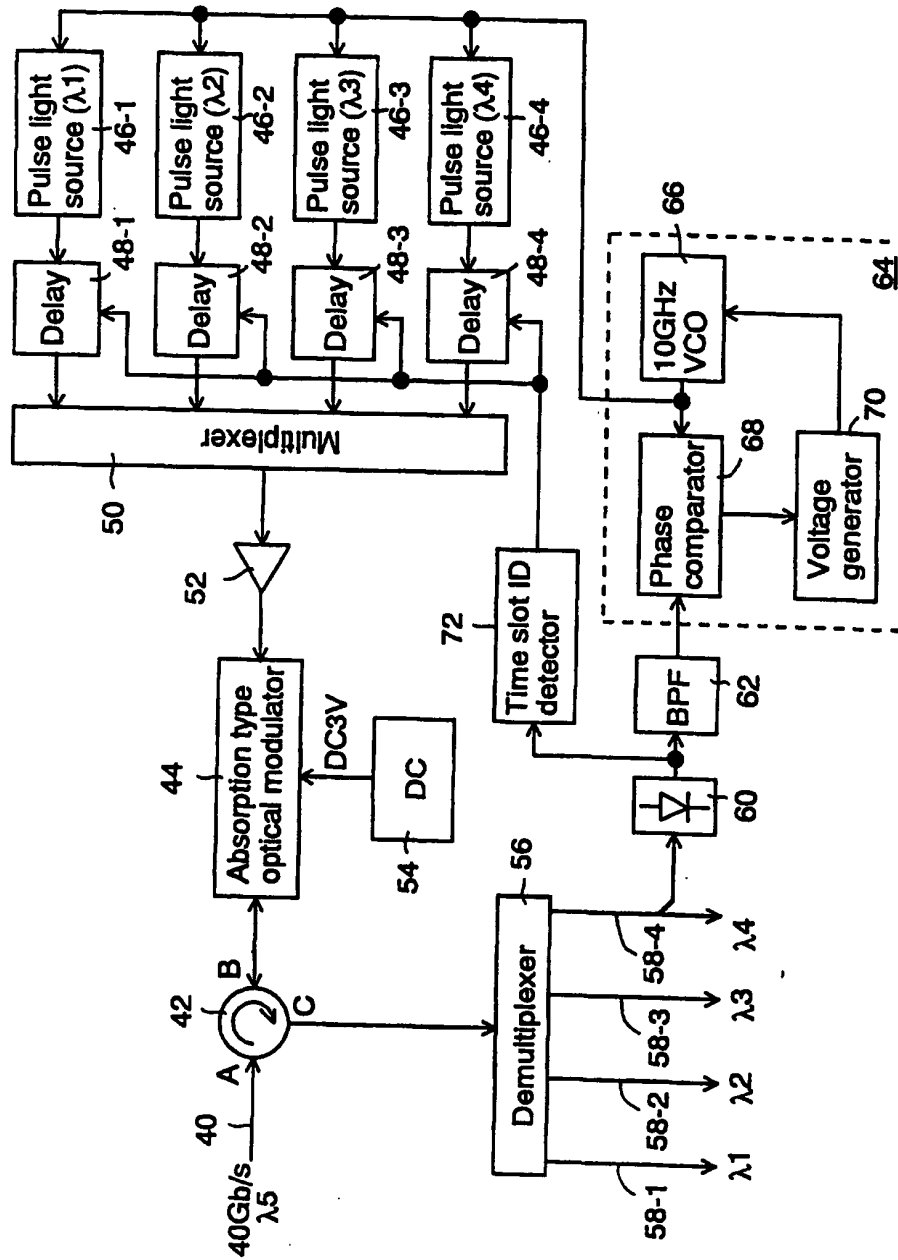
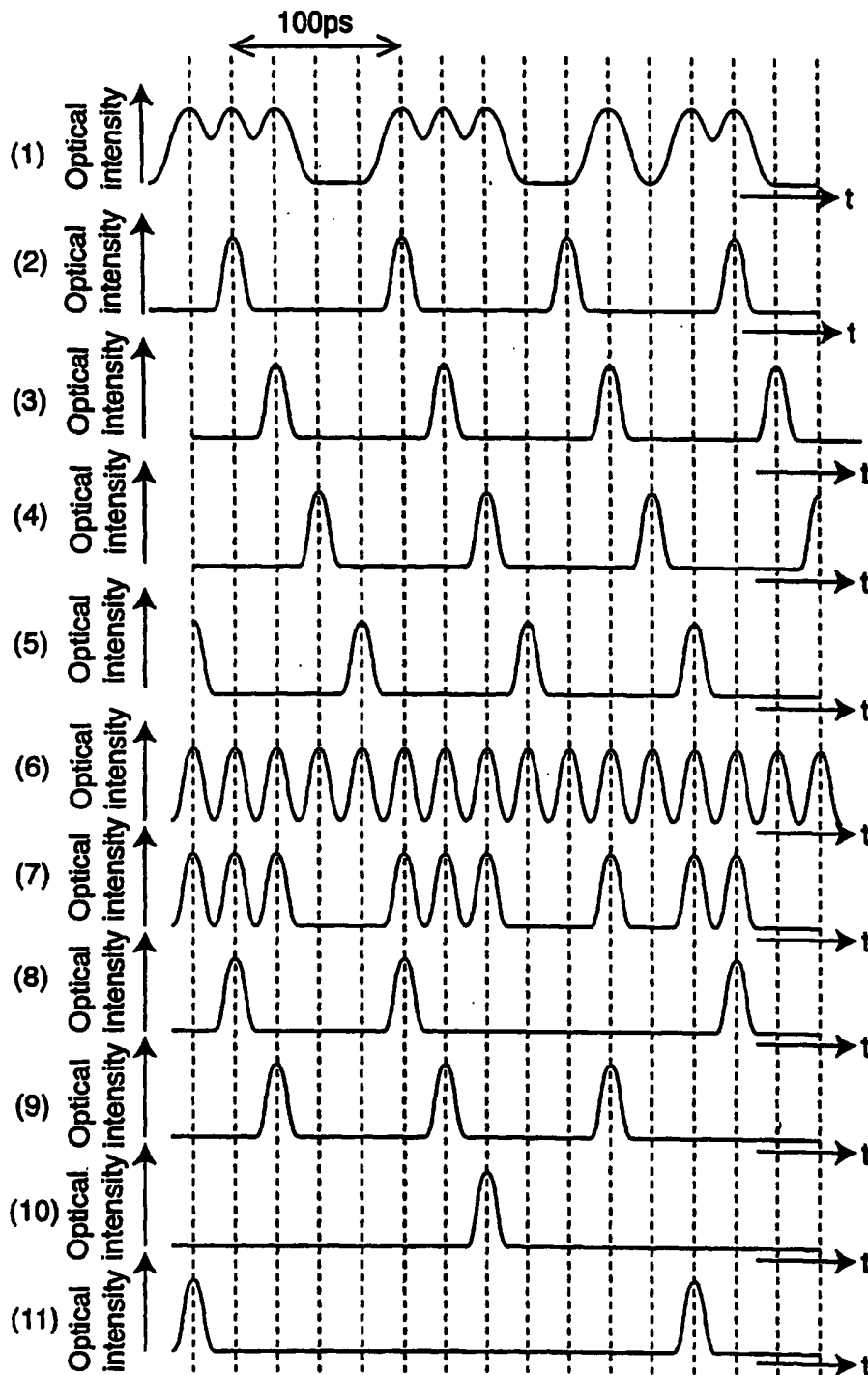


FIG. 5



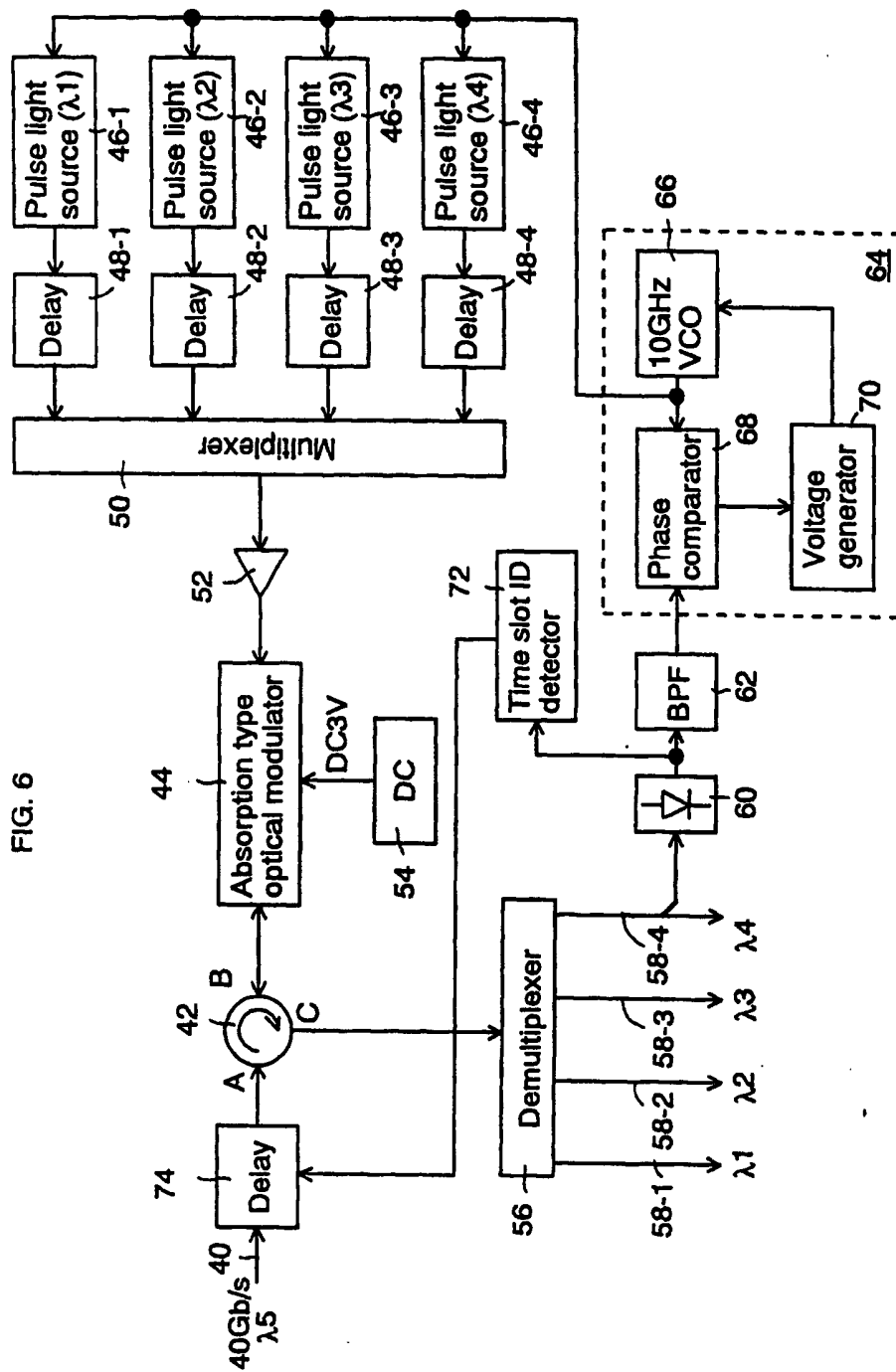
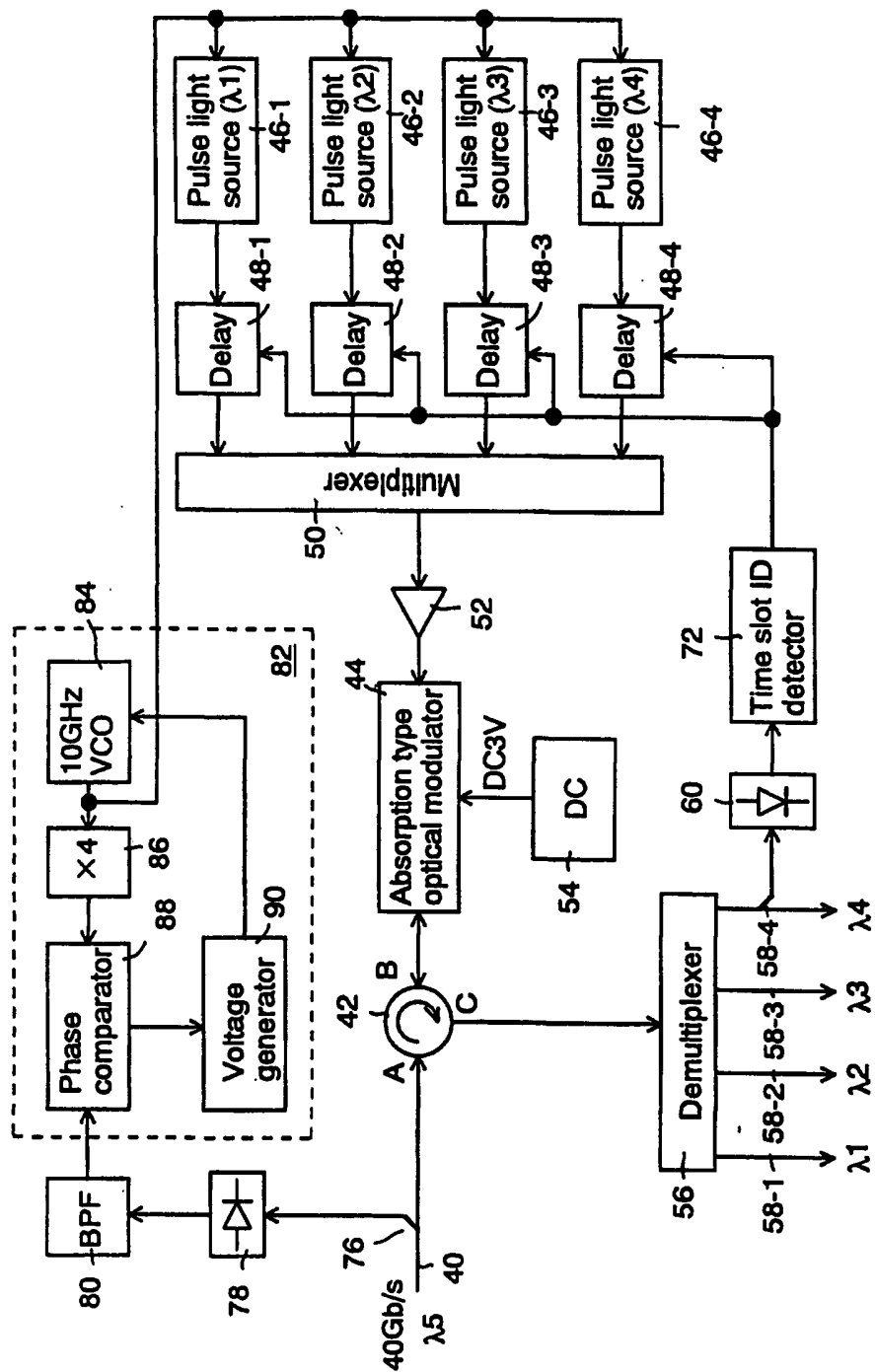


FIG. 7





Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) EP 1 137 213 A3

(12) EUROPEAN PATENT APPLICATION

(88) Date of publication A3:
18.12.2002 Bulletin 2002/51

(51) Int Cl.7: H04J 14/02, H04J 14/08

(43) Date of publication A2:
26.09.2001 Bulletin 2001/39

(21) Application number: 01107260.0

(22) Date of filing: 23.03.2001

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE TR
Designated Extension States:
AL LT LV MK RO SI

• KDD Submarine Cable Systems Inc.
Tokyo (JP)

(72) Inventors:
• Suzuki, Masatoshi
Kamifukuoka-shi, Saitama (JP)
• Edagawa, Noboru
Kamifukuoka-shi, Saitama (JP)

(30) Priority: 24.03.2000 JP 2000083997

(71) Applicants:
• DDI Corporation
Tokyo (JP)

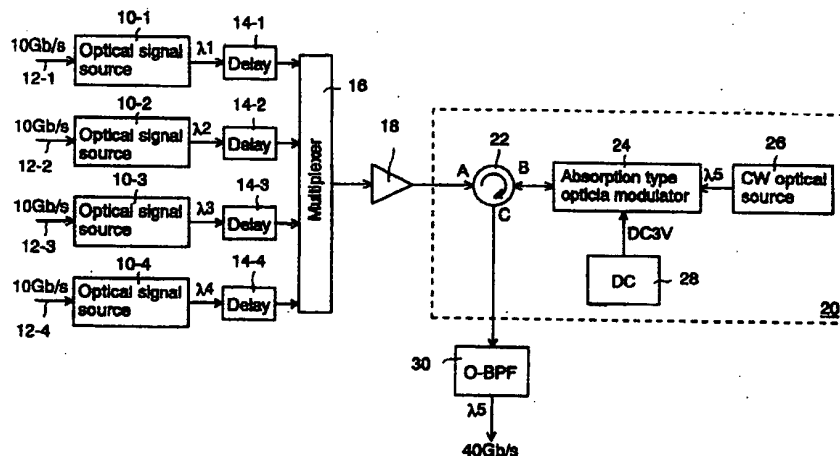
(74) Representative: Schaumburg, Thoenes & Thurn
Postfach 86 07 48
81634 München (DE)

(54) Optical TDM multiplexer, optical TDM demultiplexer, WDM/TDM converter and TDM/WDM converter

(57) An optical TDM multiplexing apparatus to multiplex a plurality of input signals in the optical stage in the time domain according to the invention comprise a plurality of signal light sources to generate optical signals each having a wavelength different from the others to carry each of the plurality of the input signals, a timing adjuster to adjust timings between the respective optical

signals so that each optical signal output from the plurality of the signal light sources is disposed on a time slot different from the others in the time domain, an optical multiplexer to multiplex each optical signal output from the timing adjuster in the wavelength domain and a wavelength converter to convert each wavelength of the output light from the optical multiplexer into a pre-determined wavelength.

FIG. 1



EP 1 137 213 A3



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 01 10 7260

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X A	EP 0 876 020 A (ALSTHOM CGE ALCATEL) 4 November 1998 (1998-11-04) * column 1, line 44 - column 2, line 41 * * column 3, line 18 - column 4, line 38; figures 1,2 * * column 5, line 41 - column 6, line 41 * * column 7, line 36 - column 8, line 37; figure 6 * * column 9, line 5; figures 8,10,11 * ---	1-6,15, 22 7-14, 16-21	H04J14/02 H04J14/08
X A	EP 0 835 035 A (ALSTHOM CGE ALCATEL) 8 April 1998 (1998-04-08) * column 1, line 3 - line 26 * * column 2, line 46 - column 4, line 18; figures 1,2 * ---	4-6 2,3,5,6	
X	PATENT ABSTRACTS OF JAPAN vol. 1996, no. 11, 29 November 1996 (1996-11-29) -& JP 08 195732 A (NIPPON TELEGR &TELEPH CORP <NTT>). 30 July 1996 (1996-07-30) * abstract * ---	7,14,15, 22	TECHNICAL FIELDS SEARCHED (Int.Cl.7) H04J H04L
E	EP 1 130 456 A (FUJITSU LTD) 5 September 2001 (2001-09-05) * column 26, line 29 - column 27, line 29; figure 15 * ---	1,3,4,6	
D,A	EP 0 828 174 A (KOKUSAI DENSHIN DENWA CO LTD) 11 March 1998 (1998-03-11) * column 1, line 5 - line 14 * * column 2, line 40 - column 4, line 46; figures 1,9 * --- -/--	1-22	
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 24 October 2002	Examiner Roldán Andrade, J
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1500 03/02 (PUB/01)



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 01 10 7260

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
A	<p>PATENT ABSTRACTS OF JAPAN vol. 1996, no. 10, 31 October 1996 (1996-10-31) -& JP 08 163096 A (NIPPON TELEGR &AMP;TELEPH CORP &LT;NTT&GT;), 21 June 1996 (1996-06-21) * abstract *</p> <p>-----</p>	1-6	
			TECHNICAL FIELDS SEARCHED (Int.Cl.7)
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 24 October 2002	Examiner Roldán Andrade, J
<p>CATEGORY OF CITED DOCUMENTS</p> <p>X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document</p> <p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document</p>			

EPO FORM 1503 03/02 (904/371)

**ANNEX TO THE EUROPEAN SEARCH REPORT
ON EUROPEAN PATENT APPLICATION NO.**

EP 01 10 7260

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

24-10-2002

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0876020 A	04-11-1998	FR 2762732 A1	30-10-1998
		CA 2233270 A1	28-10-1998
		EP 0876020 A1	04-11-1998
		JP 10336135 A	18-12-1998
		US 6307658 B1	23-10-2001
EP 0835035 A	08-04-1998	FR 2754409 A1	10-04-1998
		CA 2216010 A1	07-04-1998
		EP 0835035 A1	08-04-1998
		JP 10150679 A	02-06-1998
		US 5953142 A	14-09-1999
JP 08195732 A	30-07-1996	JP 3201566 B2	20-08-2001
EP 1130456 A	05-09-2001	JP 2001249371 A	14-09-2001
		EP 1130456 A2	05-09-2001
		US 2001021288 A1	13-09-2001
EP 0828174 A	11-03-1998	JP 10078595 A	24-03-1998
		EP 0828174 A2	11-03-1998
		US 5959764 A	28-09-1999
JP 08163096 A	21-06-1996	JP 3201564 B2	20-08-2001

EPO FORM P0458

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82